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98-16-05  
PATENT APPLICATION  
10/826,959

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James D. Ballew et al.  
Serial No.: 10/826,959  
Filing Date: April 15, 2004  
Title: System and Method for Detecting and Managing  
HPC Node Failure

**Mail Stop: Amendments**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
Dear Sir:

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I hereby certify that this communication is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to The Commissioner For Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Willie Jiles  
Willie Jiles

Date: August 15, 2005

**INFORMATION DISCLOSURE STATEMENT**

Applicants respectfully request, under 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Copies of all references are enclosed for the convenience of the Examiner. No representation is made that a search has been made, that these references are material to the patentability, or that these references qualify as prior art.

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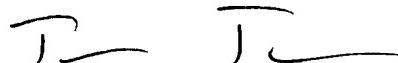
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**REMARKS**

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement, and this Information Disclosure Statement is submitted before a first Office Action on the merits. Under 37 C.F.R. § 1.97(b)(3), no fee is due. Nonetheless, the Commissioner is hereby authorized to charge any fee and credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTT S L.L.P.  
Attorneys for Applicants



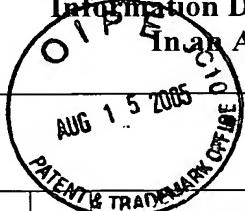
Travis W. Thomas  
Reg. No. 48,667

Date: August 15, 2005

**Correspondence Address:**

**Customer Number 45507**

PTO-1449  Information Disclosure Citation In an Application		Application No. 10/826,959	Applicant(s) James D. Ballew et al.
		Docket Number 064747.1015	Group Art Unit
			Filing Date April 15, 2004



## U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A						
B						
C						
D						

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES

## NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012031, 6 pages, 08/01/2005 and Written Opinion of the International Search Authority, 5 pages	08/01/2005
F	Chang et al., "Performance Improvement of Allocation Schemes for Mesh-Connected Computers," Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, vol. 52, no. 1, July 10, 1998, 30 pages	07/10/1998
G	Wanqian, et al. "Non-Contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers," Supercomputing '94, Proceedings, Washington, D.C., November 14-18, 1994, 10 pages	11/14/1994 - 11/18/1994
H	Krevat et al., "Job Scheduling for the BlueGene/L System," Lecture Notes in Computer Science, vol. 2537, July 24, 2002, 18 pages	07/24/2002
I		
J		

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.